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WAYNE WOLF is Professor of Electrical Engineering and Associated Faculty in the Department of Computer Science at Princeton University. His research interests include embedded computing, multimedia systems, VLSI and computer-aided design. He is the author of *Computers as Components: Principles of Embedded Computer System Design* and *Modern VLSI Design, Third Edition*.

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Edition, offers authoritative, up-to-the-minute guidance for the entire VLSI design process—from architecture and logic design through layout and packaging. Wayne Wolf has systematically updated his award-winning book for today ' s newest technologies and highest-value design techniques. Wolf introduces powerful new IP-based design techniques at all three levels: gates, subsystems, and architecture. He presents deeper coverage of logic design fundamentals, clocking and timing, and much more. No other VLSI guide presents as much up-to-date information for maximizing performance, minimizing power utilization, and achieving rapid design turnarounds.

This work presents an up-to-date view of VLSI design techniques for custom digital integrated circuit design. The text aims to show how to design a variety of digital chips - ranging from CPUs to interface logic -

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starting with only bare silicon. It covers all phases of the IC design process and provides an insight into how CAD methods should be used. Readers will be helped to understand the complete IC design process, from defining what the chip does, to designing layout and preparing the chip for manufacturing tests.

The award-winning VLSI design guide is now fully updated to reflect the latest advances in chip design

Techniques for the latest deep-submicron, mega-chip projects. The start-to-finish, state-of-the-art guide to VLSI design. VLSI design is system design. To build high-performance, cost-effective ICs, you must understand all aspects of digital design, from planning and layout to fabrication and packaging. Modern VLSI Design, Second Edition:

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Devices and layouts: transistor structures and characteristics, wires, vias, parasitics, design rules, layout design and tools. Logic gates and combinational logic networks, including interconnect delay and crosstalk. Sequential machines and sequential system design.

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minimizing power consumption at every level of abstraction, from circuits to architecture and new insights into design-for-testability techniques that maximize quality despite quicker turnarounds. It also presents detailed coverage of the algorithms underlying contemporary VLSI computer-aided design software, so designers can understand their tools nomatter which ones they choose. Whether you're a practicing professional or advanced student, this is the sophisticated VLSI design knowledge you need to succeed with tomorrow's most challenging projects.

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