

Vcs Mx User Guide

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VCS @ MX/VCS MXi User Guide G-2012.09 September 2012 Comments? E-mail your comments ab out this manual to: vcs_support@synopsys.com.

VCS MX/VCS MXi User Guide - EECS Instructional Support ...

The following manuals contain related information about VCS MX:
• VCS MX Reference Guide A manual that provides reference information for VCS MX, and is intended to be used in conjunction with the VCS MX User Guide.
• VCS MX Quick Reference A small booklet that helps you locate reference information quickly and easily.

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3-2 Chapter 3: Synopsys VCS and VCS MX Support VCS and VCS MX Guidelines Quartus II Handbook Version 13.1 November 2012 Altera Corporation Volume 3: Verification Add the -lca option for Stratix V and later families because they include IEEE-encrypted simulation files for VCS and VCS MX. Add -timescale=1ps/1ps to ensure picosecond resolution.

3. Synopsys VCS and VCS MX Support - Intel

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build/vcs-sim-rtl directory contains a makefile which can build Verilog simulators and run tests on these simulators. 3 Manual VCS Build Process We will rst manually go through the commands for VCS using the commandline so that you can see the steps required to make VCS work. Since this process is tedious we will only do it once, later we will use

RTL Simulation using Synopsys VCS

The Synopsys VCS® functional verification solution is the primary verification solution used by a majority of the world’s top 20 semiconductor companies. VCS provides the industry's highest performance simulation and constraint solver engines.

VCS Functional Verification Solution - Synopsys

vcs-mx-b-2008.12: vcs_vY-2006.06: vcs_vY-2006.06-11: vera_vA-2007.12-9 : This entry was posted in Services, Software and tagged Commercial Software, software, Synopsys. Bookmark the permalink.

Synopsis | User Guide

From VCS User guide: During elaboration, VCS MX builds the design hierarchy. By default, when you recompile the design, VCS MX compiles only those design units that have changed since the last elaboration. This is called incremental compilation. The incremental compilation feature is the default in VCS MX.

system verilog - VCS incremental elaboration feature ...

Intended Audience¶. The Accelerator Functional Unit (AFU) Accelerator Simulation Environment (ASE) User Guide addresses both beginning and experienced developers. To be successful, you should have knowledge and experience in the following areas: C/C++; Verilog/SystemVerilog; RTL simulators such as Synopsys VCS-MX* or Mentor Graphics ModelSim-SE* or QuestaSim*

Intel® Accelerator Functional Unit (AFU) Simulation ...

You can also obtain the VCS MX User Guide from the Synopsys website. To compile the libraries individually, follow these steps: Identify all the IP variations and create a directory for each of them. Each directory holds the intermediate VCS library files for its IP variation.

Error-[MPD] Module previously declared

3 Manual VCS Build Process We will rst manually go through the steps for performing gate-level simulation with VCS using the com-mandline. Since this process is tedious we will only do it once, later we will use scripts to automate the steps in this portion of the ow for us. For now, you should be able to cut and paste the commands below into

Gate-Level Simulation using Synopsys VCS

Cisco TelePresence MX700 and MX800 Administrator Guide 1. Connect to the video system 2. Sign in Enter the user name and password for your video system and click Sign In.

Cisco TelePresence MX700 and MX800 Administrator Guide (TC7.2)

If it doesn't tell you what you want to know, we suggest you look through the documentation provided by Synopsis. There is a VCS user guide available on the machines in the directory: /usr/local/packages/synopsys_2012/vcs-mx/doc/UserGuide/pdf/vmm_user_guide.pdf . This user guide includes another tutorial for the VCS tools.

EE 382N: VCS Manual

vcs-user-guide.pdf - User guide for Synopsys VCS virsim-user-guide.pdf - User guide for Synopsys waveform viewer Getting started Before using the 6.375 tool ow you must add the course locker and run the course setup script with the following two commands. % add 6.375 % source /mit/6.375/setup.csh. 6.375 Tutorial 1, Spring 2006 2 VCS

Verilog and VCS

This book is designed both for FPGA users interested in developing new, specific components - generally for reducing execution times -and IP core designers interested in extending their catalog of specific components. The main focus is circuit synthesis and the discussion shows, for example, how a given algorithm executing some complex function can be translated to a synthesizable circuit description, as well as which are the best choices the designer can make to reduce the circuit cost, latency, or power consumption. This is not a book on algorithms. It is a book that shows how to translate efficiently an algorithm to a circuit, using techniques such as parallelism, pipeline, loop unrolling, and others. Numerous examples of FPGA implementation are described throughout this book and the circuits are modeled in VHDL. Complete and synthesizable source files are available for download.

Verilog and its usage has come a long way since its original invention in the mid-80s by Phil Moorby. At the time the average design size was around ten thousand gates, and simulation to validate the design was its primary usage. But between then and now designs have increased dramatically in size, and automatic logic synthesis from RTL has become the standard design ?ow for most desgin. Indeed, the language has evolved and been re-standardized too. Overtheyears,manybookshavebeenwrittenaboutVerilog.Myown.coauthored with Phil Moorby, had the goal of de?ning the language and its usage, providing - amples along the way. It has been updated with ?ve new editions as the language and its usage evolved. However this new book takes a very different and unique view; that of the designer. John Michael Williams has a long history of working and teaching in the ?eld of IC and ASIC design. He brings an indepth presentation of Verilog and how to use it with logic synthesis tools; no other Verilog book has dealt with this topic as deeply as he has. If you need to learn Verilog and get up to speed quickly to use it for synthesis, this book is for you. It is sectioned around a set of lessons including presentation and explanation of new concepts and approaches to design, along with lab sessions.

Now fully updated for Cisco’s new CIPTV2 300-075 exam, Implementing Cisco IP Telephony and Video, Part 2 (CIPTV2) Foundation Learning Guide is your Cisco® authorized learning tool for CCNP® Collaboration preparation. Part of the Cisco Press Foundation Learning Series, it teaches advanced skills for implementing a Cisco Unified Collaboration solution in a multisite environment. The authors show how to implement Uniform Resource Identifier (URI) dialing, globalized call routing, Intercluster Lookup Service and Global Dial Plan Replication, Cisco Service Advertisement Framework and Call Control Discovery, tail-end hop-off, Cisco Unified Survivable Remote Site Telephony, Enhanced Location Call Admission Control (CAC) and Automated Alternate Routing (AAR), and important mobility features. They introduce each key challenge associated with Cisco Unified Communications (UC) multisite deployments, and present solutions-focused coverage of Cisco Video Communication Server (VCS) Control, the Cisco Expressway Series, and their interactions with Cisco Unified Communications Manager. Each chapter opens with a topic list that clearly identifies its focus, ends with a quick-study summary of key concepts, and presents review questions to assess and reinforce your understanding. The authors present best practices based on Cisco Solutions Reference Network Designs and Cisco Validated Designs, and illustrate operation and troubleshooting via configuration examples and sample verification outputs. This guide is ideal for all certification candidates who want to master all the topics covered on the CIPTV2 300-075 exam. Shows how to craft a multisite dial plan that scales, allocates bandwidth appropriately, and supports QoS Identifies common problems and proven solutions in multisite UC deployments Introduces best practice media architectures, including remote conferencing and centralized transcoding Thoroughly reviews PSTN and intersite connectivity options Shows how to provide remote site telephony and branch redundancy Covers bandwidth reservation at UC application level with CAC Explains how to plan and deploy Cisco Device Mobility, Extension Mobility, and Unified Mobility Walks through deployment of Cisco Video Communication Server and Expressway series, including user and endpoint provisioning Covers Cisco UCM and Cisco VCS interconnections Shows how to use Cisco UC Mobile and Remote Access Covers fallback methods for overcoming IP WAN failure Demonstrates NAT traversal for video and IM devices via VCS Expressway Introduces dynamic dial plan learning via GDPR, SAD, or CCD

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A comprehensive study aid for Web developers, this authoritative volume offers everything intermediate and advanced Flash developers need to become Macromedia certified, from learning how to identify requirements to designing and coding ActionScript, to testing, deploying, implementing, and troubleshooting Flash MX 2004 applications. Original. (Advanced)

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Verilog and VCS

Now fully updated for the new Cisco CAPP5 300-085 exam, Implementing Cisco Collaboration Applications (CAPP5) Foundation Learning Guide is your Cisco® authorized learning tool for CCNP® Collaboration preparation. Part of the Cisco Press Foundation Learning Series, it teaches advanced skills for designing, deploying, configuring, and troubleshooting Cisco Collaboration and Unified Communications applications, devices, and networks. Author Chris Olsen shows how to effectively use Cisco Unity Connection, Cisco Unity Express, Cisco Instant Message and Presence, Cisco TelePresence Video Communication Server, and Cisco TelePresence Management Suite in production environments. He begins by introducing the server platforms and overlays that are the basis for all Cisco Unity Connection design and integration. Next, he presents in-depth coverage of a wide range of essential tasks—from user configuration to voicemail redundancy, configuring Cisco Jabber Mobile, to provisioning Cisco Prime Collaboration. Each chapter opens with a list of topics that clearly identifies its focus. Each chapter ends with a summary of key concepts for quick study, as well as review questions to assess and reinforce your understanding. Throughout, configuration examples and sample verification outputs illustrate critical issues in network operation and troubleshooting. Whether you are preparing for the CCNP Collaboration certification exams or you are just interested in learning about how to deploy and operate Cisco collaboration applications, you will find this book to be an invaluable resource. Shows how to integrate Cisco Unity Connection with Cisco Unified Communications Manager or other PBXs Covers configuring Cisco Unity Connection users, templates, service classes, distribution lists, security, LDAP, dial plans, and call management Walks through Unified Messaging single Inbox configuration Shows how to design, integrate, and configure feature-rich branch office messaging solutions with Cisco Unity Express Explains Cisco Unified IM and Presence components, design, integration, deployment, and feature configuration Covers Cisco Jabber and Cisco Jabber Mobile configuration Guides you through deploying Cisco Collaboration Systems Applications with Cisco Prime Collaboration Introduces Cisco TelePresence Management Suite (Cisco TMS) capabilities and scheduling options This book is in the Foundation Learning Guide Series. These guides are developed together with Cisco® as the only authorized, self-paced learning tools that help networking professionals build their understanding of networking concepts and prepare for Cisco certification exams.

Verilog and VCS

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